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U.S. UTILITY Patent Application

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PATENT NUMBER and  
ISSUE DATE

APPL NUM 10016008	FILING DATE 12/11/2001	CLASS 439 428	SUBCLASS 689	GAU 763 1772	EXAMINER DEO
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\*\*APPLICANTS: Ushiki Takehito; Tsunoda Hitoshi;

\*\*CONTINUING DATA VERIFIED:  
THIS APPLICATION IS A DIV OF 09/346,004 07/01/1999  
g r

abandoned 4/16/02

\*\* FOREIGN APPLICATIONS VERIFIED:  
JAPAN 10-208633 07/08/1993  
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<input checked="" type="checkbox"/> PG-PUB	<input type="checkbox"/> DO NOT PUBLISH	<input type="checkbox"/> RESCIND	ATTORNEY DOCKET NO
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiners's initials		<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no g r	
TITLE : Semiconductor wafer, method for producing the same, and wafer chuck			

U.S. DEPT. OF COMM /PAT. & TM-PTO-436L (Rev. 12-94)

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Assistant Examiner	Total Claims Print Claim for O.G.
<b>ISSUE FEE</b>		Primary Examiner	<b>DRAWING</b> Sheets Draw. Figs. Draw. Print Fig.
Amount Due	Date Paid		
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		PREPARED FOR ISSUE	Application Examiner
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